

U.S. Department of Commerce, Patent and Trademark Office				Application No.: 10/601,237		
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing date: June 19, 2003		
SEP 11 2005				Inventors: Laurence B. Boucher, et al.		
HIGH PERFORMANCE NETWORK INTERFACE				Group Art Unit: 2143		
				Examiner name: Jerry B. Dennison		
				Attorney Docket No. ALA-008F		

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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2		5,535,375	7-96	Eshel	709	27	
3		5,633,780	05-1997	Cronin	361	220	
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5		5,742,765	04-1998	Wong et al.	709	235	
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7		5,848,293	12-1998	Gentry	709	5	
8		5,872,919	2-99	Wakeland	709	235	
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10		5,920,566	07-1998	Hendel et al.	370	401	
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Examiner /Jerry Dennison/ Date Considered 03/22/2010

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH U.D./
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OPTIONAL SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing date: June 19, 2003
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HIGH PERFORMANCE NETWORK INTERFACE		Examiner name: Jerry B. Dennison
		Attorney Docket No. ALA-008F

U.S. Patent Documents

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Foreign Patent Documents

		Document Number	Date	Country	Class	Subclass	Translation
	A	WO 01/59966	8/16/01	PCT/US00/06475			Yes
	B	WO 01/86430	11/15/01	PCT/US01/15180			No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

1	iSCSI HBA article entitled "iSCSI and 2Gigabit fibre Channel Host Bus Adapters from Emulex, QLogic, Adaptec, JNI", 8 pages, printed 10/01/01.
2	U.S. Provisional Patent Application No.: 60/283,896, Titled: CRC Calculations for Out of Order PUDs, Filed April 12, 2003, Inventor: Amit Oren, Assignee: Siliquent Technologies Ltd.
3	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
4	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.
5	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.
6	Thia, Y.H. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control", Protocols for High Speed Networks, pages 53-68, 1993.
7	Thia, Y.H. Publication entitled "A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture", Protocols for High Speed Networks, pages 224-239, 1995.

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